



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichio YUDASAKA et al.

Application No.: 09/936,040 ✓

Filed: December 11, 2001

For: **SYSTEM AND METHOD FOR MANUFACTURING
A THIN FILM TRANSISTOR**

Batch/Confirmation No.: 7811

Allowed: March 26, 2004

Group Art Unit: 2826

Examiner: Ahmed N. Sefer

Docket No.: 110554

LETTER TO THE OFFICIAL DRAFTSPERSON

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Please substitute the attached 18 sheets of formal drawings depicting Figures 1-18 for the corresponding drawings filed with the application.

Respectfully submitted,


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JAO:YSC/dmw

Date: April 8, 2004

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